

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ROBERT N. HASBUN, ET AL.

Application No.: 10/654,559

Filed: September 2, 2003

For: **Method of Performing Reliable Updates in
a Symmetrically Blocked Nonvolatile
Memory Having a Bifurcated Storage
Architecture**

Art Group: 2189

Examiner: Elmore, Reba I

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

09/07/2006 NGUYEN1 00000007 10654559

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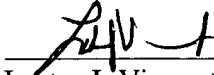
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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

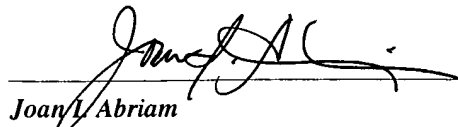
Date: August 30, 2006



Lester J. Vincent, Reg. No. 31,460

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (408) 720-8300

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